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**REQUEST  
FOR  
CONTINUED EXAMINATION (RCE)  
TRANSMITTAL**Address to:  
Commissioner for Patents  
Mail Stop: RCE  
Alexandria, Virginia 22313-1450

Application Number	09/649,437 / 6896
Filing Date	August 23, 2000
First Named Inventor	Bart Reynolds
Art Unit	2123
Examiner Name	Eduardo Garcia Otero
Attorney Docket Number	X-1770 US

**This is a Request for Continued Examination (RCE) under 37 C.F.R. 1.114 of the above-identified application.**

Request for Continued Examination (RCE) practice under 37 CFR 1.114 does not apply to any utility or plant application filed prior to June 8, 1995, or to any design application. See Instruction Sheet for RCEs (not to be submitted to the USPTO) on page 2.

**1. Submission required under 37 C.F.R. 1.114**a. ☐ Previously submitted

- i. ☐ Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on \_\_\_\_\_  
(Any unentered amendment(s) referred to above will be entered).
- ii. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on \_\_\_\_\_
- iii. ☐ Other \_\_\_\_\_

b. ☒ Enclosed

- i. ☒ Amendment/Reply
- ii. ☐ Affidavit(s)/Declaration(s)
- iii. ☐ Information Disclosure Statement (IDS)
- iv. ☒ Other Return Receipt Postcard

**2. Miscellaneous**

- a. ☐ Suspension of action on the above-identified application is requested under 37 C.F.R. § 1.103(c) for a period of \_\_\_\_\_ months. (Period of suspension shall not exceed 3 months; Fee under 37 C.F.R. § 1.17(i) required)
- b. ☐ Other \_\_\_\_\_

**3. Fees** The RCE fee under 37 C.F.R. § 1.17(e) is required by 37 C.F.R. § 1.114 when the RCE is filed.a. ☒ The Director is hereby authorized to charge the following fees, or credit any overpayments, to Deposit Account No. 24-0040

- i. ☒ RCE fee required under 37 C.F.R. 1.17(e) in the amount of \$770.00
- ii. ☒ Extension of time fee (37 C.F.R. 1.136 and 1.17) Within First Month \$110.00
- iii. ☐ Other \_\_\_\_\_

b. ☐ Check in the amount of \$ \_\_\_\_\_ enclosedc. ☐ Payment by credit card (Form PTO-2038 enclosed)

08/23/2004 AWONDAF1 00000083 240040 09649437

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Name (Print /Type)	Kim Kanzaki	Registration No. (Attorney/Agent)	37,652
Signature		Date	August 17, 2004

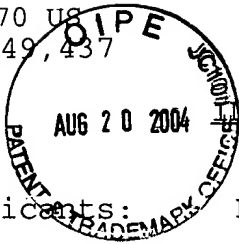
**CERTIFICATE OF MAILING OR TRANSMISSION**I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail / EXPRESS MAIL in an envelope addressed to: Commissioner for Patents, Mail Stop: RCE, P.O. Box 1450, Alexandria, Virginia 22313-1450, or facsimile transmitted to the U.S. Patent and Trademark Office on: 08-17-04

Name (Print/Type)	Pat Slaback	Date	August 17, 2004
Signature			

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X-1770 US  
09/649,437

PATENT  
Conf. No.: 6896



Applicants: Bart Reynolds et al.  
Assignee: Xilinx, Inc.  
Title: Method and Apparatus for Specifying  
Addressability and Bus Connections in a Logic  
Design  
Serial No.: 09/649,437 File Date: 08-23-00  
Examiner: Eduardo Garcia Otero Art Unit: 2123  
Docket No.: X-1770 US Conf. No.: 6896

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Mail Stop RCE  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed from the Patent Office on April 22, 2004, please replace/substitute the following claims as indicated.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 9 of this paper.

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